

**TITLE**

**APPARATUS AND METHOD FOR  
CONSERVING POWER IN A MONITOR**

**CLAIM OF PRIORITY**

**[0001]** This application makes reference to, incorporates the same herein, and claims all benefits accruing under 35 U.S.C. § 119 from my application *APPARATUS FOR SAVING POWER IN MONITOR AND METHOD THEREOF* filed with the Korean Intellectual Property Office on 14 July 2001 and there duly assigned Serial No. 42599/2001.

**BACKGROUND OF THE INVENTION**

**Field of the Invention**

**[0002]** The present invention relates to a power control system of a monitor, and more particularly, to an apparatus and a method for conserving power in a monitor by controlling the power supplied to a signal sensor.

**Description of the Related Art**

**[0003]** There have been efforts for reducing power consumed by electric and electronic products, to decrease the consumption of electric energy generated using the oil and atomic power and for reducing environmental pollution. Among these electric and electronic products, a display device, a peripheral device of a computer system, uses high voltage so that lots of efforts have been made

1 to reduce its power consumption.

2 [0004] In general, monitors are peripheral devices connected to a computer system for displaying  
3 information input by a user or data generated in a computer on their screens. In order to reduce  
4 power consumption in such monitors, the screens of the monitors must automatically turn off when  
5 not being used.

6 [0005] Generally, a display device comprises control means for saving consumption power. In one  
7 approach, the control means maybe implemented by a Display Power Management System (*DPMS*)  
8 mode, proposed by Video Electronics Standard Association (VESA) in U.S.A.

9 [0006] The *DPMS* mode functions to manage power of a display device, which is one of the  
10 peripheral devices of a computer, according to a used state of the computer to save the power.

11 [0007] In conformity with the VESA, the computer selectively supplies or blocks *horizontal* and  
12 vertical synchronous signals to the display device according to its used state, and the display device  
13 manages power according to the presence of the *horizontal* and vertical synchronous signals from  
14 the computer.

15 [0008] The power management states are classified into an on state, a stand-by state, a suspend  
16 state and a power off state. Both the *horizontal* and vertical synchronous signals are applied at the  
17 on state, and only the vertical synchronous signal is applied at the stand-by state. Only the *horizontal*  
18 synchronous signal is applied at the suspend state, and either the *horizontal* or vertical synchronous  
19 signal is not applied at the power off state.

20 [0009] The power management state is sequentially changed to the on state stand-by state suspend  
21 state power off state with the continuous lapse of unused time of the computer. It is commonly  
22 prescribed that consumption power of the display device be about 80 W at the on state, 65 W or less  
23 at the stand-by state, 25 W or less at the suspend state and 5 W or less at the power off state.

## SUMMARY OF THE INVENTION

**[0010]** It is a first object of the present invention to provide a power saving apparatus in a monitor, which is capable of maximizing the amount of power conserved by cutting the power supplied to a signal sensor at predetermined time intervals during a power saving mode.

**[0011]** It is a second object of the present invention to provide a method for controlling monitor power for the above power saving apparatus.

**[0012]** Accordingly, to achieve the first object, there is provided a power saving apparatus in a power control system of a monitor. The power saving apparatus includes a power supply unit which supplies operational power to each block of the monitor, including, a signal sensor which senses a video synchronization signal input into the signal sensor and generates data regarding the input state of a signal, a signal driver which converts a video signal into a display driving signal, a display which displays the video signal that is converted in the signal driver on a screen, and a microprocessor which determines whether a monitor is in a power saving mode or a normal mode based on whether or not a synchronization signal exists in the data on the input state of a signal generated by the signal sensor and then cuts off the power supplied from the power supply unit to the various blocks, and cutting power to the signal sensor at predetermined time intervals if the monitor is determined to be in a power saving mode.

**[0013]** To achieve the second object, there is provided a method for controlling the power of a monitor including determining whether the monitor is in a power saving mode or a normal mode based on whether or not horizontal or vertical video synchronization signals exist, and cutting operational power of blocks of the monitor and simultaneously supplying power to a signal sensor, which senses signal input, and cutting power from the signal sensor at predetermined time intervals

1 if the monitor is determined to be in a power saving mode, and supplying power to each of the  
2 blocks of the monitor if the monitor is determined to be in a normal mode.

### 3 BRIEF DESCRIPTION OF THE DRAWINGS

4 [0014] A more complete appreciation of the present invention, and many of the attendant  
5 advantages thereof, will become readily apparent as the same becomes better understood by  
6 reference to the following detailed description when considered in conjunction with the  
7 accompanying drawings in which like reference symbols indicate the same or similar components,  
8 wherein:

9 [0015] FIG. 1 is a block diagram illustrating a power saving apparatus of a monitor according to  
10 the present invention;

11 [0016] FIG. 2 is a timing diagram illustrating time periods during which power is supplied and  
12 cut-off as controlled by the power saving apparatus shown in FIG. 1; and

13 [0017] FIG. 3 is a flow chart illustrating a method for controlling the power of a monitor  
14 according to the present invention.

### 15 DETAILED DESCRIPTION OF PREFERRED EMBODIMENT

16 [0018] Referring to FIG. 1, a power saving apparatus of a monitor includes a power supply unit  
17 210, a power switching unit 220, a microprocessor 230, a signal sensor 240, a signal driver 260 and  
18 a display 270 . The signal sensor 240, the signal driver 260, and the display 270 are connected to  
19 the power switching unit 220 and all are controlled by the microprocessor 230.

20 [0019] The power supply unit 210 supplies power to each block of a monitor using a power  
21 converter, such as an AC/DC adaptor or a DC/DC converter.

1 [0020] The signal sensor 240 detects a video (horizontal and/or vertical) synchronization signal  
2 input thereto and senses whether or not a signal is received. At the same time, the signal sensor 240  
3 generates data regarding the input state of the video synchronization signal, such as the width of the  
4 video synchronization signal, the period of the video synchronization signal, or whether or not the  
5 video synchronization signal exists.

6 [0021] The signal driver 260 receives a video signal and converts the video signal into a display  
7 driving signal to be applied to the display 270. The display 270 may be a CRT or LCD for displaying  
8 the video signal converted in the signal driver 260 on a screen.

9 [0022] The power switching unit 220 simultaneously or sequentially switches power supplied  
10 from the power supply unit 210 to the signal sensor 240, the signal driver 250, and the display 260  
11 according to a switching control signal of the microprocessor 230.

12 [0023] The microprocessor 230 identifies whether or not the horizontal synchronization signal  
13 and/or the vertical synchronization signal exist using the data received from the signal sensor 240  
14 regarding the input state of the video synchronization signal. If the monitored synchronization signal  
15 exists, a monitor is determined to be in a normal mode by the microprocessor 230. If the monitored  
16 synchronization signal is absent, a monitor is determined to be in a power saving mode by the  
17 microprocessor 230.

18 [0024] In a power saving mode, the microprocessor 230 controls power switching unit 220 to cut  
19 off the power supplied from the power supply unit 210 to the signal driver 260 and the display 270.  
20 Additionally, the microprocessor 230 controls power switching unit 220 to cut off the power  
21 supplied from the power supply unit 210 to the signal sensor 240 at predetermined time intervals.  
22 On the other hand, in a normal mode, the microprocessor 230 allows power to be supplied from the  
23 power supply unit 210 to the signal sensor 240, the signal driver 260, and the display 270.

1 [0025] Therefore, in the power saving mode, the microprocessor 230 controls the period of time  
2 for which power is supplied from the power supply unit 210 to the signal sensor 240 and the period  
3 of time for which the power supplied from the power supply unit 210 to the signal sensor 240 is cut-  
4 off. Accordingly, in the power saving mode, the signal sensor 240 is capable of sensing a video  
5 synchronization signal input thereto by using the power supplied at predetermined time intervals.

6 FIG. 2 is a timing diagram illustrating the time periods corresponding to when power is  
7 supplied to and cut-off from the signal sensor 240, as controlled by microprocessor 230 and power  
8 switching unit 220 shown in FIG. 1.

9 [0026] Referring to FIG. 2,  $t_a$  represents the time period during which power is supplied to the  
10 signal sensor 240, and  $t_b$  represents the time period during which the power supplied to the signal  
11 sensor 240 is cut-off. As shown, the microprocessor 230 controls and power switching unit 220 to  
12 provide and cuts off the power supplied to the signal sensor 240 at predetermined time intervals in  
13 a power saving mode. Preferably, the period of time for which the power supplied to the signal  
14 sensor is cut-off is longer than the period of time for which power is supplied to the signal sensor  
15 240.

16 [0027] FIG. 3 is a flow chart illustrating a method for controlling the power of a monitor  
17 according to the present invention. In step 410, a register of the microprocessor 230 is initialized  
18 when power is turned on.

19 [0028] In step 430, it is determined whether a monitor is in a power saving mode or a normal  
20 mode by detecting whether or not a video synchronization signal, such as the horizontal  
21 synchronization signal and/or the vertical synchronization signal, are input to signal sensor 240. The  
22 result of the determination made by signal sensor 240 is stored in the register of microprocessor 230.  
23 Accordingly, the value stored in the register is determinative of whether a monitor is in a power

1 saving mode or a normal mode.

2 **[0029]** If the monitor is determined to be in a power saving mode, the power supplied to the signal  
3 sensor 240, the signal driver 260, and the display 270 are simultaneously or sequentially cut-off in  
4 step 440. Next, after a predetermined period of time (period  $t_b$  of FIG. 2) during which the power  
5 supplied to the signal sensor 240 is cut-off in step 460, power is supplied to the signal sensor 240  
6 for another predetermined period of time (period  $t_a$  of FIG. 2) in step 470. The steps 430 through  
7 470 are continuously repeated until it is determined in step 430 that the mode has changed from the  
8 power saving mode to the normal mode.

9 **[0030]** If the monitor is determined to be in a normal mode in step 430, power is simultaneously  
10 or sequentially supplied to the signal sensor 240, the signal driver 260, and the display 270 in step  
11 480, and then the power supply to the signal sensor 240, the signal driver 260, and the display 270  
12 is maintained until the power is turned off in step 490 or it is determined in step 430 that the monitor  
13 is in the power saving mode.

14 **[0031]** As described above, according to the present invention, it is possible to maximize the  
15 amount of power conserved in a monitor power device by additionally controlling the power  
16 supplied to a signal sensor, which is not controlled in the prior art, in a power saving mode.